

IN THE SPECIFICATION:

Please replace paragraph 30 with the following paragraph showing changes.

Turning now to FIGURE 2, illustrated is a block diagram of an embodiment of a quadrature oscillator buffer, generally designated 200, constructed according to the principles of the present invention. The quadrature oscillator buffer 200 includes a bias current generator 210, a plurality of transistors 220, a common source 230, an inductor 240, a capacitor 250, resonant loads 260, 261, 262, 263, 264 coupled to a power supply voltage 270 and a first capacitance 280. The quadrature oscillator buffer 200 provides a quadrature-phase output signal OUT_Q and an in-phase output signal OUT_I .